

CV



Dirk Beernaert (B) is Head of Unit G1 – "Nanoelectronics" in the European Commission – Directorate General for Information Society and Media (DG INFSO) and Executive Director - at interim - of the Joint Undertaking ENIAC.

Dirk is an engineer in physics, in nuclear science and in material science (University of Ghent – Belgium -1976). Before joining the European Commission he was involved in statistics and as engineering and technology manager in setting up a laboratory to sustain the design of microelectronic components and subsequently in setting up a manufacturing site in micro-electronics to produce digital, analogue and high voltage components. He was responsible for a team dealing with technology development and technology transfer between different organisations.

He has joined the European Commission in 1990 where he has been responsible for research initiatives in micro-electronics under different European Frameworks (FP) for Research and for cooperation with Eureka in that field. He also was responsible for setting up the workplan for research in e-work, e-business, e-commerce (FP5). He has started a Unit and a Program dealing with 'Integrated Micro and Nanosystems' (FP 5 and 6) including Microsystems, sensors, interfaces, displays and large area integration and for setting up the workplan for Photonics at the start of the 7th Framework. He is now responsible for running within Directorate General Information Society the Unit of 'nano-electronics' dealing with the implementation of Framework 6 and 7 activities, with planning the future activities and with all related research, innovation stimulation and regulatory activities in these fields. He is recently nominated Executive Director - at interim - of the first public private partnership between industry, European Member States and the European Commission in the field of Nanoelectronics (The Joint Undertaking ENIAC).

Owner of 2 patents and author of more than 100 articles on diverse research topics, on commercialisation, on innovation and research strategies mainly in the field of micro-nano-technologies and miniaturisation.